



2818

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Lee et al.

Examiner: Mai, Son Luu

Serial No: 10/081,546

Group Art Unit: 2818

#8 election

Filed: 21 February, 2002

Docket: 8729-214 (SS-15661-US)

M. Braun

5/24/03

For: **DATA INPUT CIRCUIT AND MEHTOD FOR SYNCHRONOUS
SEMICONDUCTOR MEMORY DEVICE**

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the office action dated April 23, 2003, applicant provisionally elects claims 8-18, 19-23, and 24-28, with traverse. Claims 1-40 are pending in the current application.

As set forth in MPEP § 803, if the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though the application contains claims to independent or distinct inventions.

CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an addressed to: Mail Stop: Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 23, 2003.

Dated: 5/23/03

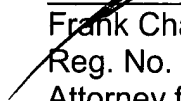

Frank Chau

While the invention of Species I, Species II, and Species III may be distinct for the reasons set forth by the Examiner, it is respectfully submitted that simultaneous examination will not present an undue burden, much less any burden.

For example, the claimed subject matters are all related to circuits in a synchronous semiconductor memory device. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See MPEP § 803.

Early and favorable consideration of this application is earnestly solicited.

Respectfully Submitted

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